

FORM PTO-1449 (Modified) (Rev. 7-80)		U.S. Dept. of Commerce Patent and Trademark Office		Atty Docket No.: NSC1-G0610 [P04402 P01]		Appln. No.: NEW	
INFORMATION DISCLOSURE CITATION (Use several sheets if necessary)				Applicant(s): KAMESH V. GADEPALLY			
				Filing Date: Herewith		Group: Unknown	

U.S. PATENT DOCUMENTS							
*Examiner Initials	Document Number	Date	Name	Class	Subclass	Filing Date	
m	AA	5,911,114	06/08/1999	NAEM	438	684	03/21/1997
↓	AB	5,970,370	10/19/1999	BESSER ET AL.	438	586	12/08/1998
↓	AC	6,096,599	08/01/2000	KEPLER ET AL.	438	249	11/06/1998
m	AD	6,136,705	10/24/2000	BLAIR	438	682	10/22/1998
	AE						
	AF						
	AG						

FOREIGN PATENT DOCUMENTS							
*Examiner Initials	Document Number	Date	Country	Class	Subclass	Translation YES NO	
	AH						
	AI						
	AJ						

OTHER DOCUMENTS		
m	AK	S. Wolf, <i>Silicon Processing for the VLSI Era</i> , Vol. I, pp. 388-399 (Lattice Press, 1986)
	AL	

Examiner <u>Quoc Hoang</u>	Date Considered <u>10/02</u>
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* Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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 12/03/01